

# Optimizing Latency and Reliability of Pipeline Workflow Applications

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# Introduction and motivation

- Mapping applications onto parallel platforms  
**Difficult challenge**
- Heterogeneous clusters, fully heterogeneous platforms  
**Even more difficult!**
- Structured programming approach
  - Easier to program (deadlocks, process starvation)
  - Range of well-known paradigms (pipeline, farm)
  - Algorithmic skeleton: help for mapping

Mapping pipeline skeletons onto heterogeneous platforms

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**Mapping pipeline skeletons onto heterogeneous platforms**

# Multi-criteria scheduling of workflows

## Workflow



Several consecutive data-sets enter the application graph.

## Multi-criteria?

**Latency:** maximal time elapsed between beginning and end of execution of a data set

**Failure:** the probability that a processor fails during execution

Bi-criteria!

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# Rule of the game

- Map each pipeline stage on a single processor
- Goal: minimize latency **AND** minimize failure probability
- Several mapping strategies



The pipeline application

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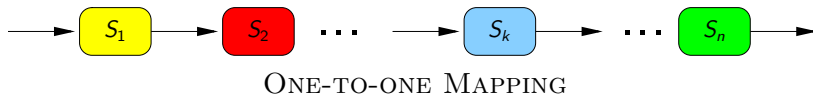
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- Replication (one interval onto several processors) in order to increase reliability

# Major Contributions

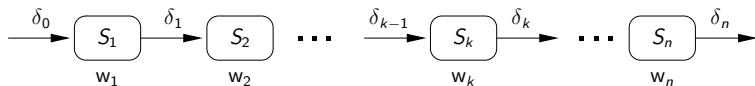
- Definition of bi-criteria mapping
- Complexity results
  - Mono-criterion problems
  - Bi-criteria problems
- Optimal algorithms

# Outline

- 1 Framework
- 2 Motivating Examples
- 3 Complexity Results
  - Mono-criterion Problems
  - Bi-criteria Problems
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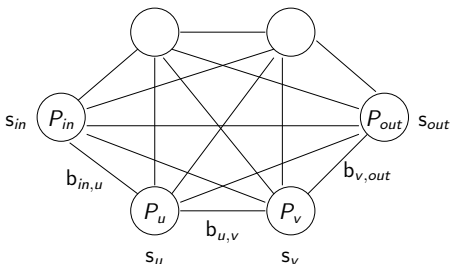


# The application



- $n$  stages  $S_k$ ,  $1 \leq k \leq n$
- $S_k$ :
  - receives input of size  $\delta_{k-1}$  from  $S_{k-1}$
  - performs  $w_k$  computations
  - outputs data of size  $\delta_k$  to  $S_{k+1}$
- $S_0$  and  $S_{n+1}$ : virtual stages representing the outside world

# The platform



- $p$  processors  $P_u$ ,  $1 \leq u \leq p$ , fully interconnected
- $s_u$ : speed of processor  $P_u$
- bidirectional link  $link_{u,v} : P_u \rightarrow P_v$ , bandwidth  $b_{u,v}$
- $fp_u$ : failure probability of processor  $P_u$  (independent of duration, meant to run for a long time)
- **one-port** model: each processor can either send, receive or compute at any time-step

# Different platforms

*Fully Homogeneous* – Identical processors ( $s_u = s$ ) and links ( $b_{u,v} = b$ ): typical parallel machines

*Communication Homogeneous* – Different-speed processors ( $s_u \neq s_v$ ), identical links ( $b_{u,v} = b$ ): networks of workstations, clusters

*Fully Heterogeneous* – Fully heterogeneous architectures,  $s_u \neq s_v$  and  $b_{u,v} \neq b_{u',v'}$ : hierarchical platforms, grids

# Different platforms

*Fully Homogeneous* – Identical processors ( $s_u = s$ ) and links ( $b_{u,v} = b$ ): typical parallel machines

*Failure Homogeneous* – Identically reliable processors ( $fp_u = fp_v$ )

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*Failure Heterogeneous* – Different failure probabilities ( $fp_u \neq fp_v$ )

# Mapping problem: INTERVAL MAPPING

- Partition of  $[1..n]$  into  $m$  intervals  $I_j = [d_j, e_j]$   
(with  $d_j \leq e_j$  for  $1 \leq j \leq m$ ,  $d_1 = 1$ ,  $d_{j+1} = e_j + 1$  for  $1 \leq j \leq m - 1$  and  $e_m = n$ )
- Interval  $I_j$  mapped onto set of processors  $P_{\text{alloc}(j)}$

$$FP = 1 - \prod_{1 \leq j \leq p} (1 - \prod_{u \in \text{alloc}(j)} fp_u)$$

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$$\mathcal{FP} = 1 - \prod_{1 \leq j \leq p} \left( 1 - \prod_{u \in \text{alloc}(j)} \text{fp}_u \right)$$

$$\mathcal{L} = \sum_{1 \leq j \leq p} \left\{ k_j \times \frac{\delta_{d_j-1}}{b} + \frac{\sum_{i=d_j}^{e_j} w_i}{\min_{u \in \text{alloc}(j)} (s_u)} \right\} + \frac{\delta_n}{b}$$

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$$\mathcal{L} = \sum_{u \in \text{alloc}(1)} \frac{\delta_0}{b_{in,u}} + \sum_{1 \leq j \leq p} \max_{u \in \text{alloc}(j)} \left\{ \frac{\sum_{i=d_j}^{e_j} w_i}{s_u} + \sum_{v \in \text{alloc}(j+1)} \frac{\delta_{e_j}}{b_{u,v}} \right\}$$



# Objective function?

## Mono-criterion

- Minimize  $\mathcal{L}$
- Minimize  $\mathcal{FP}$

## Bi-criteria

- How to define it?  
Minimize  $\alpha.\mathcal{L} + \beta.\mathcal{FP}$ ?
- Values which are not comparable
- Minimize  $\mathcal{L}$  for a fixed failure probability
- Minimize  $\mathcal{FP}$  for a fixed latency

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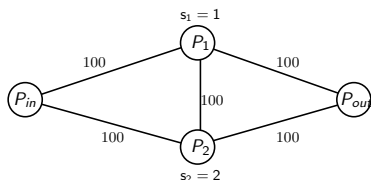
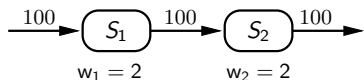
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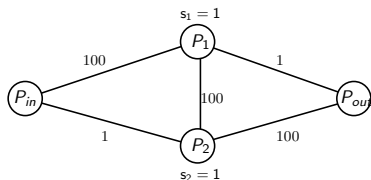
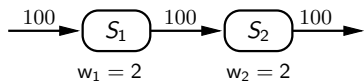
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# Mono-criterion - Interval Mapping

Minimize  $\mathcal{L}$



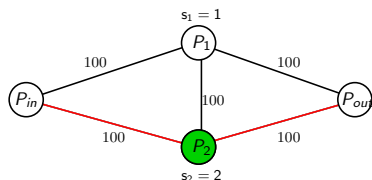
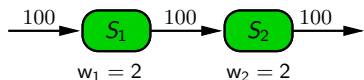
Comm. Hom. Platform



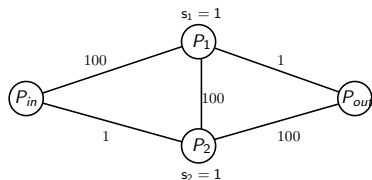
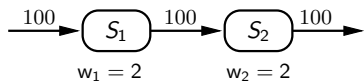
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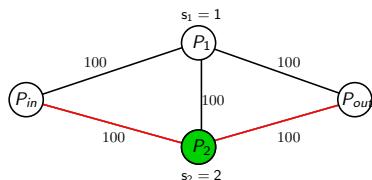
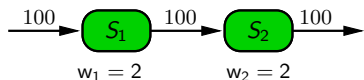
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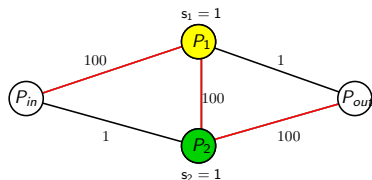
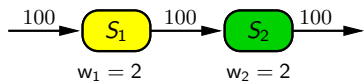
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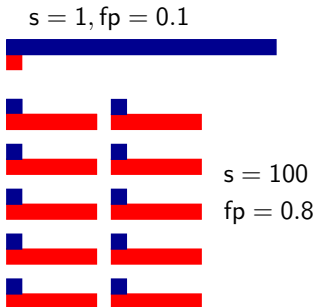
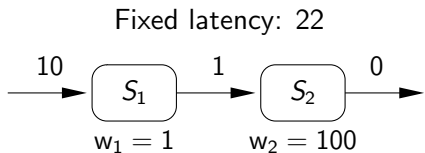
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# Bi-criteria - Interval Mapping

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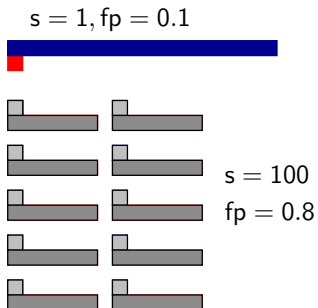
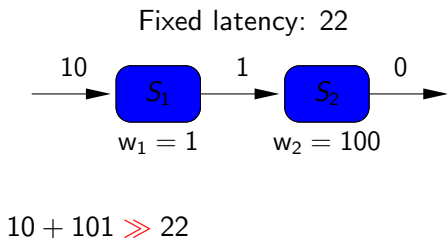
Communication homogeneous - Failure heterogeneous



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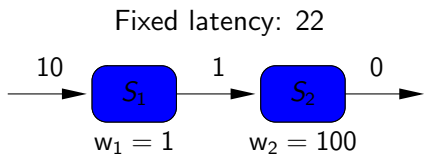
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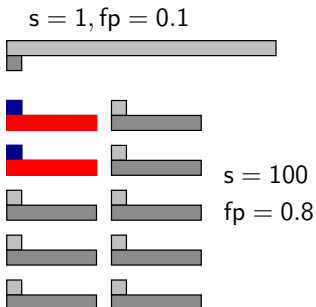
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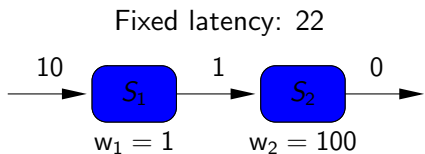
$$\mathcal{FP} = (1 - (1 - 0.8^2)) = 0.64$$



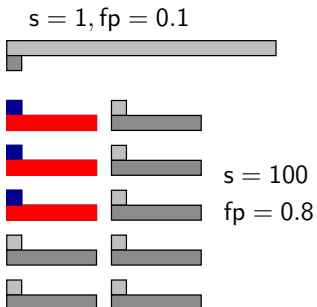
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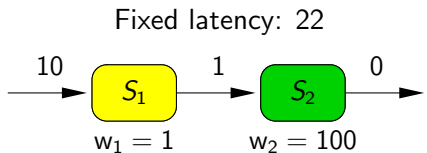
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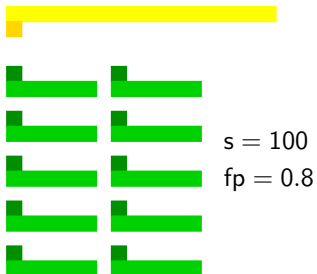
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$$10 + 1/1 + 10 \times 1 + 100/100 = 22$$

$$\mathcal{FP} : 1 - (1 - 0.1) \times (1 - 0.8^{10}) < 0.2$$

$s = 1, fp = 0.1$



# Outline

- 1 Framework
- 2 Motivating Examples
- 3 Complexity Results**
  - Mono-criterion Problems
  - Bi-criteria Problems
- 4 Conclusion

# Mono-criterion Problems

Minimize the failure probability?

## Theorem 1

Minimizing the failure probability can be done in polynomial time.

- Replicate the whole pipeline as a single interval.
- Use all processors.
- True for all platform types.

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Idea:

- Latency is optimized by suppressing all communications.
- Replication increases latency (additional communication).

Map whole pipeline on fastest processor.

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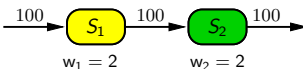
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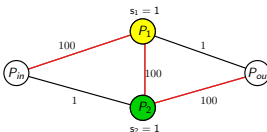
# Mono-criterion Problems

Minimize the latency?

What about *Fully Heterogeneous* platforms?



Remember example:



## Theorem 3

Minimizing the latency is NP-hard on *Fully Heterogeneous* platforms for one-to-one mappings.

# Mono-criterion Problems

But ... considering general mappings ...

## Theorem 4

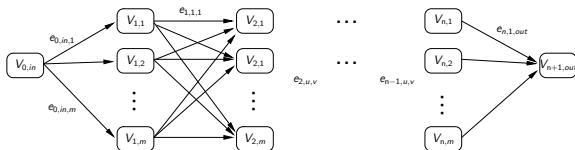
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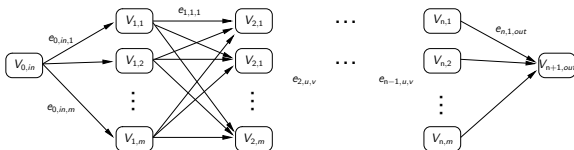
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**Optimal mapping:** Shortest path in the graph.

**Interval mapping:** still an open problem



# Bi-criteria Problems



$$1 - (1 - fp^{a+b}) \leq 1 - ((1 - fp^a)(1 - fp^b))$$

## Lemma

On *Fully Homogeneous* and *Communication Homogeneous-Failure Homogeneous* platforms, there is a mapping of the pipeline as a single interval which minimizes the failure probability under a fixed latency threshold, and there is a mapping of the pipeline as a single interval which minimizes the latency under a fixed failure probability threshold.

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# Fully Homogeneous platforms

Minimize  $\mathcal{F}\mathcal{P}$  for a fixed latency  $\mathcal{L}$

## Algorithm 1

**begin**

Find  $k$  maximum, such that

$$k \times \frac{\delta_0}{b} + \frac{\sum_{1 \leq j \leq n} w_j}{s} + \frac{\delta_n}{b} \leq \mathcal{L}$$

Replicate the whole pipeline as a single interval onto the  $k$  (most reliable) processors

**end**



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# Fully Homogeneous platforms

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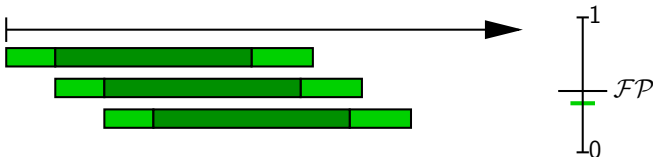
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# Other Platform Configurations

*Communication Homogeneous platforms - Failure Homogeneous*

Slightly modified *Fully Homogeneous* algorithms are optimal.

*Communication Homogeneous platforms - Failure Heterogeneous*

Lemma does not hold anymore.

Remember example.

Open problem

*Fully Heterogeneous* platforms

On *Fully Heterogeneous* platforms, the bi-criteria (decision problems associated to the) optimization problems are NP-hard.



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## Related work

- Subhlok and Vondran Latency and throughput optimization on pipeline graphs (homogeneous platforms only)
- Benoit et al. Extension of the work of Subhlok and Vondran Mapping pipelined computations onto clusters and grids DAG [Taura et al.], DataCutter [Saltz et al.]
- Energy-aware mapping of pipelined computations [Melhem et al.], three-criteria optimization
- Mapping pipelined computations onto special-purpose architectures FPGA arrays [Fabiani et al.]. Fault-tolerance for embedded systems [Zhu et al.]
- Real World Application Motion-JPEG

# Conclusion

- Bi-criteria mapping problem: latency and reliability
- Pipeline structured workflow applications
- Complexity study

## Interval Mapping

		Hom.	Com. Hom.	Hetero.
Mono-crit.	$\mathcal{L}$	polyn.	polyn.	?
	$\mathcal{FP}$	polyn.	polyn.	polyn.
Bi-crit.	$\mathcal{L} - \mathcal{FP}$ hom	polyn.	polyn.	NP
	$\mathcal{L} - \mathcal{FP}$ het	polyn.	?	NP

$\min \mathcal{L}$ , one-to-one mapping: NP

$\min \mathcal{L}$ , general mapping: polynomial

# Conclusion

- Bi-criteria mapping problem: latency and reliability
- Pipeline structured workflow applications
- Complexity study

## Interval Mapping

		Hom.	Com. Hom.	Hetero.
Mono-crit.	$\mathcal{L}$	polyn.	polyn.	?
	$\mathcal{FP}$	polyn.	polyn.	polyn.
Bi-crit.	$\mathcal{L} - \mathcal{FP}$ hom	polyn.	polyn.	NP
	$\mathcal{L} - \mathcal{FP}$ het	polyn.	?	NP

min  $\mathcal{L}$ , one-to-one mapping: NP

min  $\mathcal{L}$ , general mapping: polynomial

# Future work

## Theory

- Extension to fork, fork-join and tree workflows
- Multi-criteria: throughput in addition to reliability and latency

## Practice

- Design of multi-criteria heuristics
- Comparison of effective performance against theoretical performance
- Real experiments on heterogeneous clusters with different applications, using MPI