## Unified Model for Assessing Checkpointing Protocols at Extreme-Scale

George BOSILCA<sup>1</sup>, Aurélien BOUTEILLER<sup>1</sup>, Elisabeth BRUNET<sup>2</sup>, Franck CAPPELLO<sup>3</sup>, Jack DONGARRA<sup>1</sup>, Amina GUERMOUCHE<sup>4</sup>, Thomas HÉRAULT<sup>1</sup>, Yves ROBERT<sup>1,4</sup>, Frédéric VIVIEN<sup>4</sup>, and Dounia ZAIDOUNI<sup>4</sup>

University of Tennessee Knoxville, USA
Telecom SudParis, France
INRIA & University of Illinois at Urbana Champaign, USA
Ecole Normale Supérieure de Lyon & INRIA, France

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#### Framework

- Very very large number of processing elements (e.g., 2<sup>20</sup>)
- Failure-prone platform (like any realistic platform)
- Large application to be executed on the whole platform

 $\implies$  Failure(s) will certainly occur before completion!

• Resilience provided through checkpointing

#### Outline

- Checkpointing protocols
- 2 Coordinated checkpointing
- Hierarchical checkpointing
- Accounting for message logging

Instanciating the modelApplications

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#### Checkpointing protocols

- 2 Coordinated checkpointing
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**5** Instanciating the model

## Which checkpointing protocol to use?

#### Coordinated checkpointing

- © No risk of cascading rollbacks
- © No need to log messages
- ③ All processors need to rollback
- 🙂 Rumor: does not scale to very large platforms

#### Hierarchical checkpointing

- Seed to log inter-groups messages
  - Slowdowns failure-free execution
  - Increases checkpoint size/time
- ③ Only processors from failed group need to rollback
- © Faster re-execution with logged messages
- © Rumor: scales well to very large platforms

#### Framework

- Periodic checkpointing policies (of period T)
- Independent and identically distributed failures
- Platform failure inter-arrival time:  $\mu$
- Tightly-coupled application: progress  $\Leftrightarrow$  all processors available
- First-order approximation: at most one failure within a period

#### Waste: fraction of time not spent for useful computations



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Time spent working Time spent checkpointing

Time

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Time

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Computing the first chunk

Time spent working Time spent checkpointing

Computing the first chunk

Checkpointing the first chunk Time

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Time spent working Time spent checkpointing

Computing the first chunk

Processing the first chunk

Checkpointing the first chunk Time



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# **Blocking model:** while a checkpoint is taken, no computation can be performed



**Non-blocking model:** while a checkpoint is taken, computations are not impacted (e.g., first copy state to RAM, then copy RAM to disk)



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#### Waste in absence of failures



#### Waste in absence of failures



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#### Waste in absence of failures



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Time elapsed since last checkpoint: T

Amount of computation saved:  $(T - C) + \alpha C$ 

WASTE<sub>coord</sub>-nofailure = 
$$\frac{T - ((T - C) + \alpha C)}{T} = \frac{(1 - \alpha)C}{T}$$

#### Waste due to failures



Failure can happen

- During computation phase
- ② During checkpointing phase



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Tightly-coupled model: when one processor is victim of a failure, all processors lose their work and must roll-back to last checkpoint



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Tightly-coupled model: All processors must recover from last checkpoint

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Redo the work destroyed by the failure, that was done in the checkpointing phase before the computation phase

But no checkpoint is taken in parallel, hence this re-computation is faster than the original computation



Re-execute the computation phase



Finally, the checkpointing phase is executed

First-order approximation: we assume that no other failure occurs during the re-execution



RE-EXEC:  $\Delta - T = T_{lost} + D + R + \alpha C$ 

First-order:  $T_{lost} = \frac{1}{2}(T - C)$ 

RE-EXEC<sub>coord-fail-in-work</sub> = 
$$\frac{T-C}{2} + D + R + \alpha C$$


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Re-execute the computation phase



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#### Finally, the checkpointing phase is executed



RE-EXEC:  $\Delta - T = (T - C) + T_{lost} + D + R + \alpha C$ 

First-order approximation:  $T_{lost} = \frac{1}{2}C$ RE-EXEC<sub>coord-fail-in-checkpoint</sub> =  $(T - C) + \frac{C}{2} + D + R + \alpha C$ =  $T - \frac{C}{2} + D + R + \alpha C$ 

• Failure in the computation phase (probability:  $\frac{T-C}{T}$ )

RE-EXEC<sub>coord-fail-in-work</sub> = 
$$\frac{T-C}{2} + D + R + \alpha C$$

• Failure in the checkpointing phase (probability:  $\frac{C}{T}$ )

RE-EXEC<sub>coord-fail-in-checkpoint</sub> = 
$$T - \frac{C}{2} + D + R + \alpha C$$

$$\frac{T-C}{T}\left(\frac{T-C}{2}+D+R+\alpha C\right)+\frac{C}{T}\left(T-\frac{C}{2}+D+R+\alpha C\right)$$
$$=D+R+\alpha C+\frac{T}{2}$$

#### Overall waste

WASTE<sub>coord</sub> = WASTE<sub>coord-nofailure</sub> + 
$$\frac{1}{\mu}$$
RE-EXEC<sub>coord-failure</sub>  
=  $\frac{(1-\alpha)C}{T} + \frac{1}{\mu}\left(D + R + \alpha C + \frac{T}{2}\right)$ 

Minimize WASTE*coord* subject to:

• 
$$C \le T$$
 (by construction)  
•  $T \le 0.1\mu$  ( $\Rightarrow$  *Proba*(*Poisson*( $\frac{T}{\mu}$ )  $\ge 2$ )  $\le 0.05$ )

If  $\mu$  large enough, optimal period is  $\mathbb{T} = \sqrt{2\mu C(1-\alpha)}$  (remember Young's approximation)

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### Hierarchical checkpointing

- Processors partitioned into G groups
- Each group includes q processors
- Inside each group: coordinated checkpointing in time C(q)

Inter-group messages are logged



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When a group checkpoints, its own computation speed is slowed-down



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WASTE = 
$$\frac{T - WORK}{T}$$
 where WORK =  $T - (1 - \alpha)GC(q)$ 





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Tightly-coupled model: while one group is in downtime, none can work

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Tightly-coupled model: while one group is in recovery, none can work

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Groups must have completed the same amount of work in between two consecutive checkpoints, independently of the fact that a failure may or may not have happened on the platform in between these checkpoints. Hence, no checkpointing is possible during the rollback.



Redo work done during previous checkpointing phase and that was destroyed by the failure

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Failing group has reached the point where it previously failed, all groups now resume execution in parallel and complete the computation phase



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Finally, perform checkpointing phase



First-order:  $T_{lost} = \frac{1}{2}(T - G.C)$ 

Approximated RE-EXEC:  $\frac{T-G.C}{2} + D + R + \alpha(G - g + 1)C$ 



Approximated RE-EXEC:  $\frac{T-G.C}{2} + D + R + \alpha(G-g+1)C$ 

Average approximated RE-EXEC:

$$\frac{1}{G}\sum_{g=1}^{G}\left[\frac{T-G.C(q)}{2}+D(q)+R(q)+\alpha(G-g+1)C(q)\right]$$
$$=\frac{T-G.C(q)}{2}+D(q)+R(q)+\alpha\frac{G+1}{2}C$$

#### Failure during checkpointing phase



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### Failure during checkpointing phase



# Failure during checkpointing phase



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When does the failing group fail?

- Before starting its own checkpoint
- While taking its own checkpoint
- Ifter completing its own checkpoint

# Failure during checkpointing phase: failure before checkpoint



# Failure during checkpointing phase: failure before checkpoint



The checkpoint taken while the failure struck is that of another group; it is not affected and completes

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# Failure during checkpointing phase: failure before checkpoint



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*s*: number of groups that have successfully completed their checkpoints before the failure


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Redo work done in checkpointing phase and that was destroyed by the failure

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Failing group has reached the point where it previously failed, all groups now resume execution in parallel and complete the computation phase

Groups first complete work that was to be done during the checkpoint during which the failure occurred



Checkpointing phase completed

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RE-EXEC =  $\Delta - T$ 

 $\Delta = (T-G.C) + s.C + T_{lost} + C - T_{lost} + D + R + \alpha(G-g+1)C + (T-G.C) + \alpha(s.C + T_{lost}) + \alpha(C - T_{lost}) + (G-s-1).C$ 

 $= 2T - GC + D + R + \alpha(G - g + s + 2)C$ 



RE-EXEC =  $\Delta - T$ 

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RE-EXEC=  $\Delta - T$ 

 $\Delta = 2T - GC + D + R + \alpha(G - g + s + 2)C$ 

RE-EXEC=  $T+D+R+((\alpha-1)G+\alpha(-g+s+2)).C$ 



#### Failure during checkpointing phase



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When does the failing group fail?

- Before starting its own checkpoint
- While taking its own checkpoint
- Ifter completing its own checkpoint



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Redo work done during previous checkpointing phase and that was destroyed by the failure

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Redo work done in computation phase and that was destroyed by the failure

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Redo work done in checkpointing phase that was destroyed by the failure and that preceded the beginning of the killed checkpoint

But no checkpoint is taken in parallel, hence this re-computation is faster than the original computation



The failing group has now reached the point where it can retry taking its checkpoint

Redo work done during the checkpoint and that was destroyed by the failure

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Failing group has reached the point where it previously failed, all groups now resume execution in parallel and complete the computation phase

Failing group completes its checkpoint



Checkpointing phase completed

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RE-EXEC =  $\Delta - T$ 

 $\Delta = (T - G.C) + (g - 1)C + T_{lost} + D + R + \alpha(G - g + 1)C$  $+ (T - G.C) + \alpha(g - 1)C + T_{lost} + (C - T_{lost}) + (G - g)C$ 



RE-EXEC=  $\Delta - T$ 

 $\begin{aligned} \Delta &= (T - G.C) + (g - 1)C + T_{lost} + D + R + \alpha(G - g + 1)C \\ &+ (T - G.C) + \alpha(g - 1)C + T_{lost} + (C - T_{lost}) + (G - g)C \\ &= T + (\alpha - 1)G.C + T_{lost} + D + R + T \end{aligned}$ 



RE-EXEC =  $\Delta - T$ 

 $\Delta = (T - G.C) + (g - 1)C + T_{lost} + D + R + \alpha(G - g + 1)C + (T - G.C) + \alpha(g - 1)C + T_{lost} + (C - T_{lost}) + (G - g)C$  $= T + (\alpha - 1)G.C + T_{lost} + D + R + T$ RE-EXEC=  $T + (\alpha - 1)G.C + T_{lost} + D + R$ 





Approximation:  $T_{lost} = \frac{C}{2}$ 

Approximated RE-EXEC

$$T + (\alpha - 1)G.C(q) + \frac{C(q)}{2} + D(q) + R(q)$$

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#### Failure during checkpointing phase



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s: number of groups that have successfully completed their checkpoints before the failure, among groups that are after the failing group (including the failing group)



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Redo work done in checkpointing phase and that was destroyed by the failure

But no checkpoint is taken in parallel, hence this re-computation is faster than the original computation



Failing group has reached the point where it previously failed, all groups now resume execution in parallel

Groups first complete work that was to be done during the checkpoint during which the failure occurred

But no checkpoint is taken in parallel, hence this re-computation is
# Failure during checkpointing phase: failure after checkpoint



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Checkpointing phase completed

# Failure during checkpointing phase: failure after checkpoint



RE-EXEC =  $\Delta - T$ 

$$\Delta = (T - G.C) + (g - 1)C + s.C + T_{lost} + C - T_{lost} + D$$
  
+R + \alpha(s.C + T\_{lost}) + \alpha(C - T\_{lost}) + (G - s - g)C  
= T + D + R + \alpha(s + 1).C  
RE-EXEC= D + R + \alpha(s + 1)C

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# Failure during checkpointing phase: failure after checkpoint



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## Average waste for failures during checkpointing phase

Average  $\operatorname{Re-ExeC}$  when the failing-group g fails

• Before its checkpoint (for  $2 \le g \le G$ ): RE-EXEC<sub>before\_ckpt</sub> =  $T + D(q) + R(q) + ((\alpha - 1)G - \alpha \frac{g - 2}{2}) \cdot C(q)$ 

• During its checkpoint  
RE-EXEC<sub>during\_ckpt</sub> = 
$$T + (\alpha - 1)G.C(q) + \frac{C(q)}{2} + D(q) + R(q)$$

Solution After its checkpoint (for  $1 \le g \le G - 1$ ): RE-EXEC<sub>after\_ckpt</sub> =  $D(q) + R(q) + \alpha \frac{G - g + 3}{2}C(q)$ 

Overall average RE-EXEC: RE-EXEC<sub>ckpt</sub> =  

$$\frac{1}{G}((g-1).\text{RE-EXEC}_{before\_ckpt} + 1.\text{RE-EXEC}_{during\_ckpt} + (G-g).\text{RE-EXEC}_{after\_ckpt})$$

Average RE-EXEC when the failing-group g fails  
Overall average RE-EXEC: RE-EXEC<sub>ckpt</sub> =  
$$\frac{1}{G}((g-1).\text{RE-EXEC}_{before\_ckpt} + 1.\text{RE-EXEC}_{during\_ckpt} + (G-g).\text{RE-EXEC}_{after\_ckpt})$$

Average over all groups:

$$AVG_RE-EXEC_{ckpt} = D(q) + R(q) + \frac{G+1}{2G}T + \frac{\alpha C(q)(G+3)}{2} + \frac{C(q)(1-2\alpha)}{2G} - \frac{C(q)(G+1)}{2}$$

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$$WASTE_{hierach} = \frac{T - WORK}{T} + \frac{1}{\mu_p} \left( D(q) + R(q) + RE-EXEC \right)$$
$$= \frac{1}{2\mu_p T} \times \begin{pmatrix} T^2 \\ +GC(q) \left[ (1 - \alpha)(2\mu_p - T) + (2\alpha - 1)C(q) \right] \\ +T \left[ 2(D(q) + R(q)) + (\alpha + 1)C(q) \right] \\ +(1 - 2\alpha)C(q)^2 \end{pmatrix}$$

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Minimize WASTE *hierarch* subject to:

•  $GC(q) \leq T$  (by construction) •  $T \leq 0.1\mu \ (\Rightarrow Proba(Poisson(\frac{T}{\mu}) \geq 2) \leq 0.05)$ 

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### Impact on work

- $\bigcirc$  Logging messages slows down execution:  $\Rightarrow$  WORK becomes  $\lambda$ WORK, where  $0 < \lambda < 1$ Typical value:  $\lambda \approx 0.98$
- © Re-execution after a failure is faster:  $\Rightarrow$  RE-EXEC becomes  $\frac{\text{RE-EXEC}}{\rho}$ , where  $\rho \in [1..2]$ Typical value:  $\rho \approx 1.5$

WASTE<sub>hierarch</sub> = 
$$\frac{T - \lambda WORK}{T} + \frac{1}{\mu_p} \left( D(q) + R(q) + \frac{\text{Re-Exec}}{\rho} \right)$$

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## Impact on checkpoint size

- Inter-groups messages logged continuously
- Checkpoint size increases with amount of work executed before a checkpoint

$$C(q) = C_0(q)(1 + \beta \text{WORK}) \Leftrightarrow \beta = \frac{C(q) - C_0(q)}{C_0(q) \text{WORK}}$$
$$\text{WORK} = \lambda(T - (1 - \alpha)GC(q))$$
$$C(q) = \frac{C_0(q)(1 + \beta\lambda T)}{1 + GC_0(q)\beta\lambda(1 - \alpha)}$$

• Constraint  $GC(q) \leq T$  translates into

$${\it GC_0(q)eta\lambdalpha\leq 1} ext{ and } {\it T}\geq rac{{\it GC_0(q)}}{1-{\it GC_0(q)eta\lambdalpha}}$$

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# Outline

- Checkpointing protocols
- 2 Coordinated checkpointing
- 3 Hierarchical checkpointing
- 4 Accounting for message logging
- 5 Instanciating the model

## Three case studies

#### Coord-IO

Coordinated approach:  $C = C_{Mem} = \frac{Mem}{b_{io}}$ where Mem is the memory footprint of the application

#### Hierarch-IO

Several (large) groups, I/O-saturated  $\Rightarrow$  groups checkpoint sequentially

$$C_0(q) = rac{C_{\mathsf{Mem}}}{G} = rac{\mathsf{Mem}}{G\mathsf{b}_{io}}$$

#### **Hierarch-Port**

Very large number of smaller groups, *port-saturated*   $\Rightarrow$  some groups checkpoint in parallel  $q_{min}$  as the smallest value such that  $q_{min}b_{port} \ge b_{io}$ Groups of  $q_{min}$  processors



2 Coordinated checkpointing

Hierarchical checkpointing



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- Real matrix of size  $n \times n$  partitioned across a  $p \times p$  processor grid
- Each processor holds a matrix block of size b = n/p
- At each iteration:
  - average each matrix element with its 8 closest neighbors
  - exchange rows and columns that lie at partition boundary

- each processor sends four messages of size b
- (Parallel) work for one iteration is  $WORK = \frac{9b^2}{s_0}$

 $C(q) = C_0(q) + Logged_Msg = C_0(q)(1 + \beta WORK)$ 

Real  $n \times n$  matrix and  $p \times p$  grid  $Work = \frac{9b^2}{s_p}$ , b = n/pEach process sends a block to its 4 neighbors

#### HIERARCH-IO:

- 1 group = 1 grid row
- 2 out of the 4 messages are logged

• 
$$\beta = \frac{Logged\_Msg}{C_0(q)WORK} = \frac{2pb}{pb^2(9b^2/s_p)} = \frac{2s_p}{9b^3}$$

#### HIERARCH-PORT:

•  $\beta$  doubles



# Three applications: 2) 3D-stencil

- Real matrix of size  $n \times n \times n$  partitioned across a  $p \times p \times p$  processor grid
- Each processor holds a cube of size b = n/p
- At each iteration:
  - average each matrix element with its 27 closest neighbors
  - exchange the six faces of its cube
- (Parallel) work for one iteration is  $WORK = \frac{27b^3}{s_0}$

#### Three hierarchical variants

- HIERARCH-IO-PLANE: group = horizontal plane of size  $p^2$ :  $\beta = \frac{2s_p}{27b^3}$
- ❷ HIERARCH-IO-LINE: group = horizontal line of size p:  $\beta = \frac{4s_p}{27b^3}$
- S HIERARCH-PORT: groups of size  $q_{min}$ :  $\beta = \frac{6s_p}{27b^3}$

# Three applications: 3) Matrix product

- 3 real matrices of size  $n \times n$  partitioned across a  $p \times p$  processor grid
- Mem =  $24n^2$  (in bytes)
- Each processor holds three matrix blocks of size b = n/p
- At each iteration (Cannon's algorithm):
  - shift one block vertically and one horizontally
  - perform a matrix product
- (Parallel) work for one iteration is  $WORK = \frac{2b^3}{s_p}$ 
  - HIERARCH-IO: one group per grid row:  $\beta = \frac{s_p}{6b^3}$
  - **2** HIERARCH-PORT: groups of size  $q_{min}$ :  $\beta = \frac{s_p}{3b^3}$

# Four platforms: basic characteristics

| Name          | Number of     | Number of                     | Number of cores | Memory        | I/O Network Bandwidth (b <sub>io</sub> ) |         | I/O Bandwidth (b <sub>port</sub> ) |
|---------------|---------------|-------------------------------|-----------------|---------------|--|---------|------------------------------------|
|               | cores         | processors p <sub>total</sub> | per processor   | per processor | Read                                     | Write   | Read/Write per processor           |
| Titan         | 299,008       | 16,688                        | 16              | 32GB          | 300GB/s                                  | 300GB/s | 20GB/s                             |
| K-Computer    | 705,024       | 88,128                        | 8               | 16GB          | 150GB/s                                  | 96GB/s  | 20GB/s                             |
| Exascale-Slim | 1,000,000,000 | 1,000,000                     | 1,000           | 64GB          | 1TB/s                                    | 1TB/s   | 200GB/s                            |
| Exascale-Fat  | 1,000,000,000 | 100,000                       | 10,000          | 640GB         | 1TB/s                                    | 1TB/s   | 400GB/s                            |

| Name          | Scenario      | G (C(q))         | $\beta$ for | $\beta$ for    |
|---------------|---------------|------------------|-------------|----------------|
|               |               |                  | 2D-Stencil  | MATRIX-PRODUCT |
|               | Coord-IO      | 1 (2,048s)       | /           | /              |
| Titan         | Hierarch-IO   | 136 (15s)        | 0.0001098   | 0.0004280      |
|               | HIERARCH-PORT | 1,246 (1.6s)     | 0.0002196   | 0.0008561      |
|               | Coord-IO      | 1 (14,688s)      | /           | /              |
| K-Computer    | HIERARCH-IO   | 296 (50s)        | 0.0002858   | 0.001113       |
|               | HIERARCH-PORT | 17,626 (0.83s)   | 0.0005716   | 0.002227       |
|               | Coord-IO      | 1 (64,000s)      | /           | /              |
| Exascale-Slim | HIERARCH-IO   | 1,000 (64s)      | 0.0002599   | 0.001013       |
|               | HIERARCH-PORT | 200,0000 (0.32s) | 0.0005199   | 0.002026       |
|               | Coord-IO      | 1 (64,000s)      | /           | /              |
| Exascale-Fat  | HIERARCH-IO   | 316 (217s)       | 0.00008220  | 0.0003203      |
|               | HIERARCH-PORT | 33,3333 (1.92s)  | 0.00016440  | 0.0006407      |

## Checkpoint time

| Name          | С       |  |  |
|---------------|---------|--|--|
| K-Computer    | 14,688s |  |  |
| Exascale-Slim | 64,000  |  |  |
| Exascale-Fat  | 64,000  |  |  |

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- Large time to dump the memory
- Using 1%C
- Comparing with 0.1%C for exascale platforms
- $\alpha = 0.3$ ,  $\lambda = 0.98$  and  $\rho = 1.5$

# Plotting formulas – Platform: Titan



Waste as a function of processor MTBF  $\mu_{ind}$ 

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## Platform: K-Computer



Waste as a function of processor MTBF  $\mu_{ind}$ 

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# Plotting formulas - Platform: Exascale

#### WASTE = 1 for all scenarios!!!

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## Plotting formulas – Platform: Exascale



# Plotting formulas – Platform: Exascale with C = 1,000



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## Plotting formulas – Platform: Exascale with C = 100



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## Simulations – Platform: Titan



Makespan (in days) as a function of processor MTBF  $\mu_{ind}$ 

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## Simulations – Platform: Exascale with C = 1,000



Makespan (in days) as a function of processor MTBF  $\mu_{ind}$ , C = 1,000

## Simulations – Platform: Exascale with C = 100



Makespan (in days) as a function of processor MTBF  $\mu_{ind}$ , C = 100